

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Douglas Brisbin et al.

Application No.: 10/616,381

Filed: July 9, 2003

For: LDMOS TRANSISTOR STRUCTURE  
FOR IMPROVING HOT CARRIER  
RELIABILITY

Group Art Unit: 2811

Examiner: Unknown

**SUBMISSION OF FORMAL DRAWINGS  
IN RESPONSE TO NOTICE TO FILE  
MISSING PARTS OF NONPROVISIONAL  
APPLICATION MAILED  
OCTOBER 15, 2003**121 Spear Street, Suite 290  
San Francisco, CA 94105  
(415) 512-1312**CERTIFICATE OF MAILING**I hereby certify that this correspondence is being deposited  
with the United States Postal Service as First Class Mail in an  
envelope, addressed to: Commissioner for Patents, P.O.  
Box 1490, Alexandria, VA 22313-1450 on Nov. 5, 2003.M/S MISSING PARTS  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

STALLMAN &amp; POLLOCK LLP

Dated: 11/ 5 /2003By: Georgia K. Smith

Georgia K. Smith

Sir:

Pursuant to the Notice to File Missing Parts of Nonprovisional Application, attached for filing in the above-reference application are four (4) sheets of formal drawings to replace the original drawings filed on July 9, 2003.

Respectfully submitted,

STALLMAN &amp; POLLOCK LLP

Dated: November 4, 2003By: Brian J. KeatingBrian J. Keating  
Reg. No. 39,520

Attorneys for Applicant(s)